Post-Floorplanning Power/Ground Ring Synthesis for Multiple-Supply-Voltage Designs

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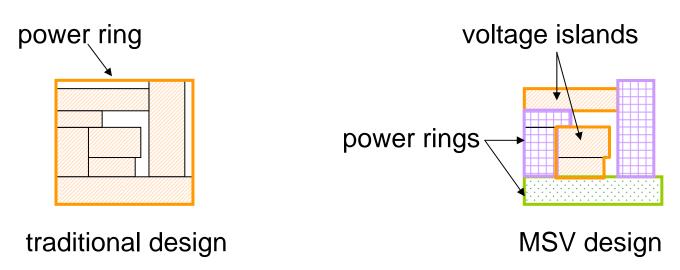
Carnegie Mellon



- Introduction
- Problem Formulation
- Algorithm
- Experiment Results
- Conclusion

Voltage Island & Power Ring

- Multiple-supply voltage (MSV) design
 - Power rings enclose the voltage islands
 - Each voltage island has its individual power ring
- MSV complicates the power-ring synthesis

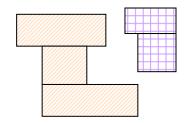


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Problem Formulation

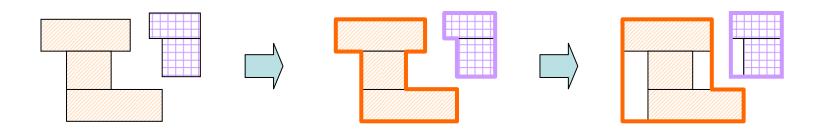
Inputs:

An MSV floorplan



Objective:

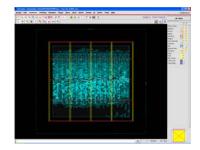
- Identify the voltage islands
- Find the power ring of each voltage island
- Minimize the number of corners in the power rings



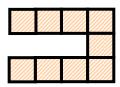
IR Drop and Corners in Power Rings

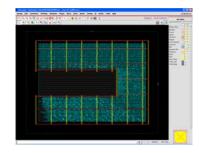
The fewer corners in power rings, the less IR drop in power rings





of corners = 4 IR drop = 4.49 e-02





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- Voltage-Island Identification
- Voltage-Island Boundary Search
- Power-Ring Corner Patching
- Experiment Results







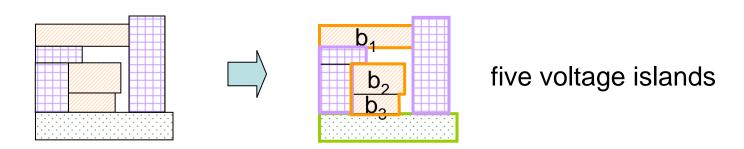






Voltage-Island Identification

- A voltage island consists of several circuit blocks
 - Operate at the same supply voltage
 - Are adjacent to at least one circuit block in the island
- Check the adjacencies block by block



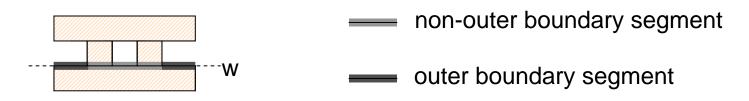
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Straightforward Approaches, but.....

- Straightforward, but <u>incorrect</u> approaches
 - Edge-overlap approach
 - If the edges overlap no other edges, they are assumed to be the contour edges
 - Cannot distinguish these lightly shaded segments

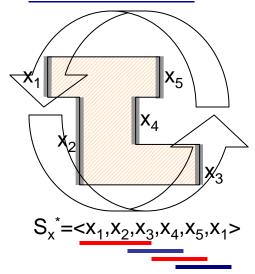


- Determine if an edge is a contour segment when the scanning line sweeps the edge
- Hardly indicates which parts are outer boundaries

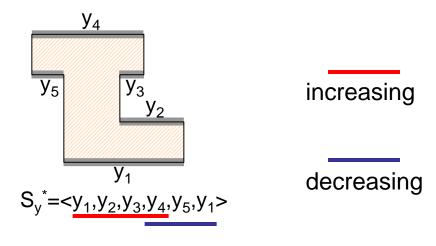


Properties of Contour Sequence

- <u>Counterclockwise</u> trace vertical and horizontal contour segments
 - From the segment with the smallest x and y coordinates



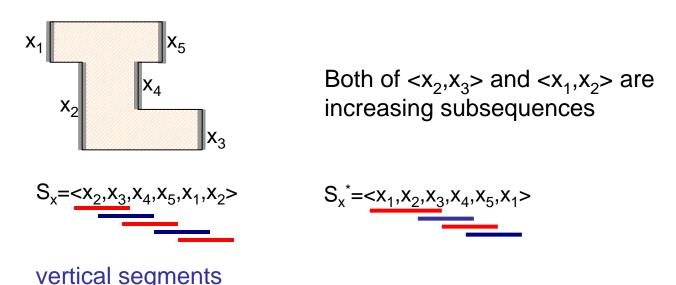
vertical segments



horizontal segments

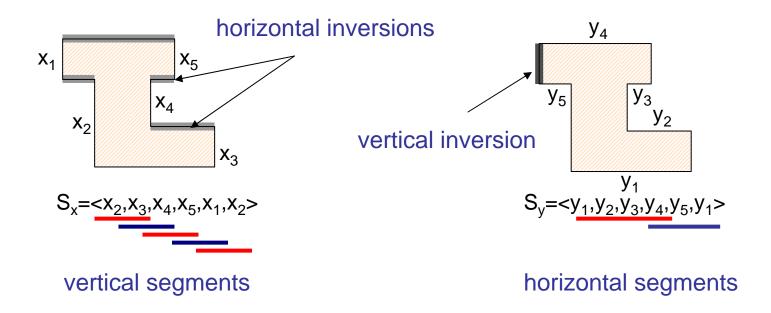
Properties of Contour Sequence

- If tracing does <u>NOT</u> start the segment with the smallest x and y coordinates
 - The sequences are still composed of alternate increasing and decreasing subsequences
 - BUT, may NOT start and end in increasing and decreasing subsequences, respectively



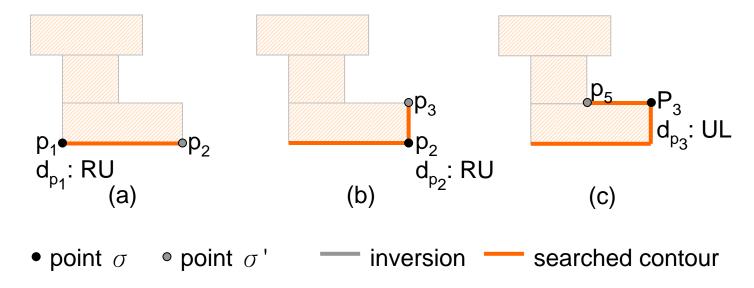
Vertical and Horizontal Inversions

 Occur when sequences change from increasing to decreasing, and vice versa



Voltage-Island Boundary Search

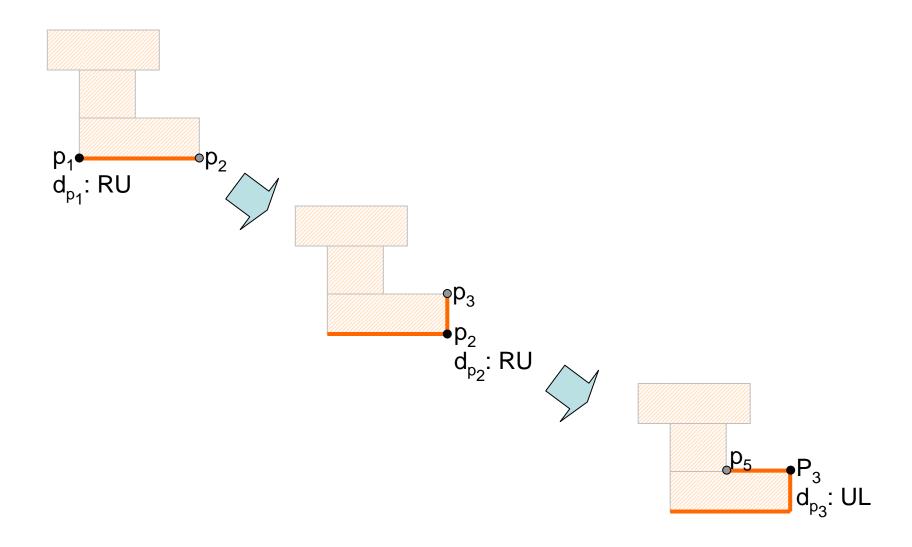
	d _{o'}		
d_{σ}	no inv.	vertical inv.	horizontal inv.
RU(RD)	RU/(RD)	DR/(UR)	UL/(DL)
LD/(LU)	LD/(LU)	UL/(DL)	DR/(UR)
UL/(UR)	UL/(UR)	LD/(RD)	RU/(LU)
DR(DL)	DR/(DL)	RU/(LU)	LD/(RD)



Correctness of Voltage-Island Boundary Search

- 1. Start at the correct beginning
 - Start at the point with the smallest y coordinate
 - Set the beginning <u>direction pair to RU</u>
- 2. Prove that the succeeding point must exist
 - It <u>must exist</u> in the directions indicated by <u>the</u> <u>direction pair</u>
- 3. Make a correct inversion detection
 - Correctly <u>detect inversions</u> if they exist
 - Correctly change the direction pair

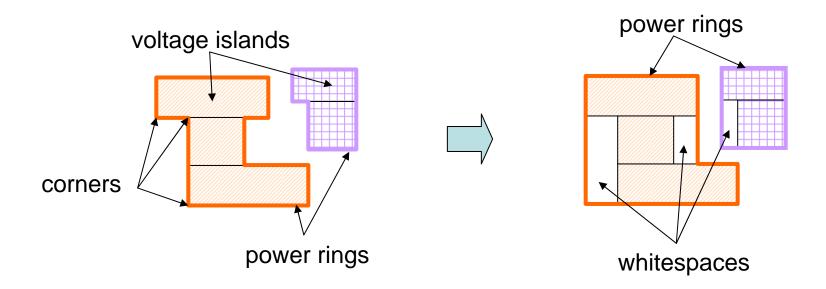
Summary



- Introduction
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Power-Ring Refinement

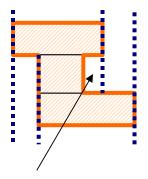
- Make power rings more regular for IR-drop reduction
- Use whitespace for power-ring refinement



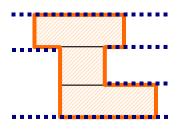
Power-Ring Corner Classification

Double joints

- Individually extend vertical and horizontal contour segments
- A double joint is enclosed by <u>one</u> extended and <u>three</u> original segments



Here is a double joint while extending vertical segments

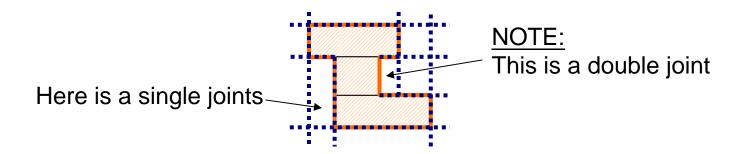


Here is no double joint while extending horizontal segments

Power-Ring Corner Classification

Single joints

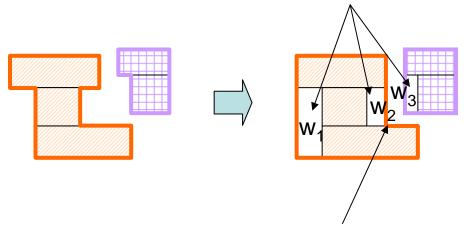
- Simultaneously extend vertical and horizontal contour segments
- A single joint is enclosed by <u>two</u> extended and <u>two</u> original segments



Complete Whitespace for a Corner

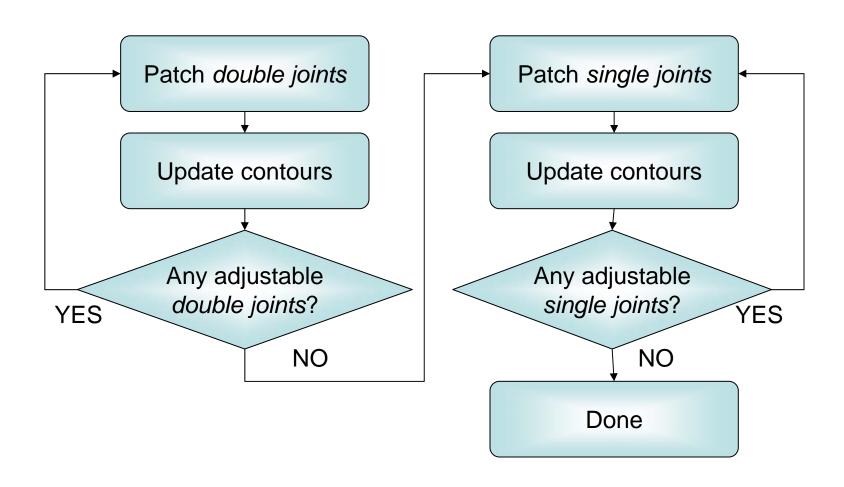
The whitespace can fill the corner

Complete whitespaces for the double/single joints



No complete whitespace for this single joint

Power-Ring Corner-Patching Flow



Optimality of Power-Ring Corner Patching

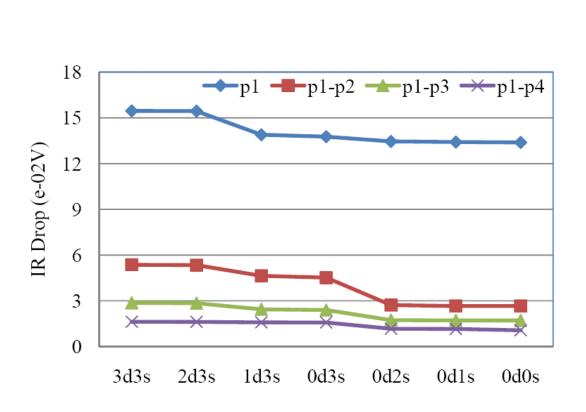
- Filling single joints will not induce double joints
 - The corner-patching flow is correct

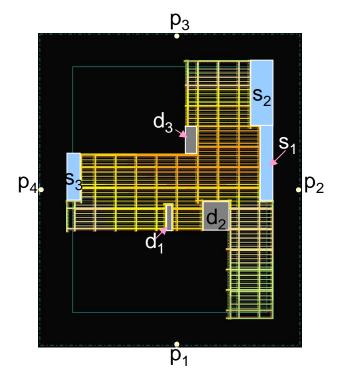
- Power-ring corner patching optimizes (minimizes)
 the # of corners in power rings
 - There is no complete whitespace left

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IR Drop & # of Corners

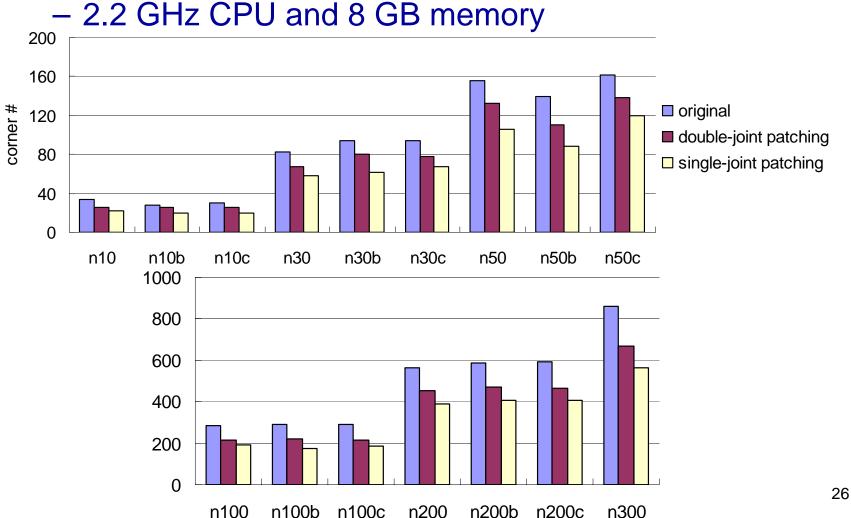
Corners in a power ring induce IR drop



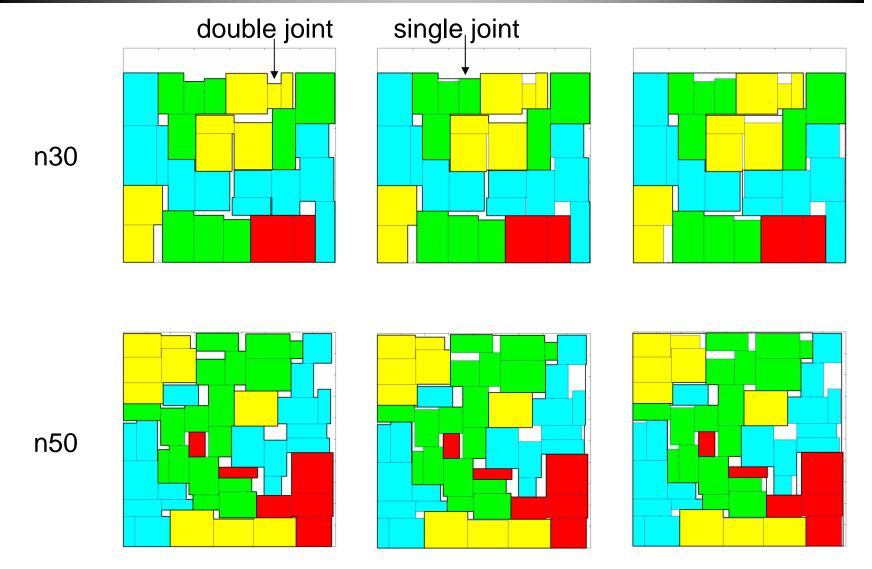


Experimental Results – Corner Patching

All running times are less than 0.06 second

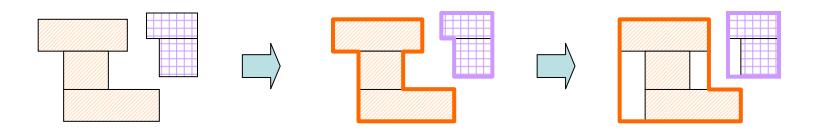


Experimental Results -- Layouts



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Conclusion



- Proposed an algorithm for power-ring synthesis for multiple-supply-voltage design
 - Voltage-Island Identification
 - Voltage-Island Boundary Search
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Correctness of Direction-Pair Switch

Assume the current direction pair is RU

