

Welcome to ISPD 2010

ACM International Symposium on Physical Design
San Francisco, CA, USA

March 14th-17th, 2010

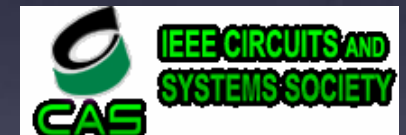
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TPC Chair: Yao-Wen Chang (Natl Taiwan U)

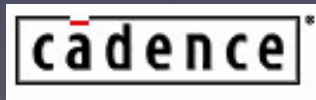
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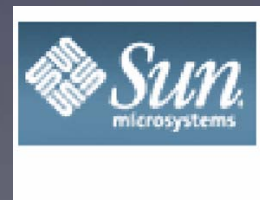


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The Technical Program

- 22 accepted papers (out of 72 initial submissions)
- Exceptionally strong slate of 13 invited Talks
 - from Cadence, IBM Research, Intel, Magma, Mentor Graphics, Synopsys, UIUC, and U Minnesota

Best Paper Award Candidates

- FOARS: FLUTE Based Obstacle-Avoiding Rectilinear Steiner Tree Construction (*Gaurav Ajwani, Chris Chu, and Wai-Kei Mak*) (Session 2)
- B-Escape: A Simultaneous Escape Routing Algorithm Based on Boundary Routing (*Lijuan Luo, Tan Yan, Qiang Ma, Martin D. F. Wong, and Toshiyuki Shibuya*) (Session 2)
- Skew Management of NBTI Impacted Gated Clock Trees (*Ashutosh Chakraborty and David Z. Pan*) (Session 7)
- SafeChoice: A Novel Clustering Algorithm for Wirelength-Driven Placement (*Jackey Z. Yan, Chris Chu, and Wai-Kei Mak*) (Session 9)

And the Winner is...

B-Escape: A Simultaneous Escape Routing Algorithm Based on Boundary Routing

(Lijuan Luo, Tan Yan*, Qiang Ma*, Martin
D. F. Wong*, and Toshiyuki Shibuya**)*

(Session 2)

** ECE Department, University of Illinois at Urbana-Champaign*

*** Fujitsu Labs of America*

The ISPD 2010 High Performance Clock Network Synthesis Contest

- Organized by Cliff Sze (*IBM Research*)
- 22 teams signed up from Universities all over the globe; 10 survived
- Sponsored in part by ACM SIGDA and by Intel Corporation
- Winners to be announced tomorrow evening (Session 7)

Logistics

- Free Ethernet-based Internet access in guest rooms
 - The \$12.95 charge you “accept” will be refunded by the hotel in your final bill
- Free wireless Internet access in Symposium room
 - Limited bandwidth (*Marriott AV11, golden11*)
 - Please focus on speaker rather than on Internet

Logistics

- ISPD is a workshop in spirit
 - Questions are encouraged
 - Interruptions are okay (unless speaker specifically requests not to do so), but speaker may elect to defer answer until later
 - Please state your name and affiliation before asking a question
- Please set your cell-phones to silent/vibrate mode
- Lunches in Spada (hotel's restaurant), Dinner Banquets in Napa Ballroom (downstairs)
 - Please wear your badge to Lunch/Dinner; it will double as your meal ticket

Logistics

- **Speakers**
 - Check in with your Session Chair in the break preceding your session
 - Keep on time
 - By default, all slides are posted (as pdf's) on the ISPD website; if this is not okay, please inform your Session Chair
- **Session Chairs**
 - Keep your sessions on time
 - Know your speakers

Thank You!

- To ACM for sponsorship, and IEEE CAS for technical co-sponsorship,
- To Cadence, IBM Research, Intel Corporation, Mentor Graphics, SpringSoft, Sun Microsystems, and Synopsys (in alphabetical order), for their generous corporate financial support,
- To the Organizing Committee for managing the logistics for this Symposium,
- To the TPC and the Steering Committee for putting together such a strong program,
- To the Contest Chair for organizing the Clock Synthesis contest,
- To the authors and speakers, and the contestants,
- **And to you; thank you for coming to San Francisco!**

Keynote

Physical Design of Biological Systems

Dr Louis K. Scheffer

Howard Hughes Medical Institute